OSSOECH OF 107 1101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chambers

Docket No: TI-30883

Serial No:

TBD

Examiner:

TBD

Filed:

07/10/01

Art Unit:

TBD

For:

CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE

HIGH-PASS TRANSFER FUNCTION

Assistant Commissioner for Patents Washington, DC 20231

Tomnie Chambers

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above-identified application, please amend the specification by inserting before the first line the sentence:

IN THE SPECIFICATION

This application claims priority under 35 USC § 119(e)(1) of provisional application Serial No. 60/217,046, filed 07/10/00.--